



FORM HDP-1449 (Based on Form PTO-1449)

**PATENT AND TRADEMARK OFFICE
INFORMATION DISCLOSURE CITATION**

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Sheet 1 of 2

ATTORNEY DOCKET NO.

MP0402

SERIAL NO.

10/789,306

APPLICANT

Aram, Farbod

FILING DATE

February 27, 2004

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2819

U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	<i>D</i>	6,466,091 B1	10/15/2002	Kejariwal, Murari et al		
2.	<i>D</i>	6,515,540 B1	2/4/2003	Prasad, Ammisetti et al		
3.	<i>D</i>	6,525,589 B1	2/25/2003	Thomsen, Axel et al		

FOREIGN PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	No
1.							

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
1.	<i>D</i>	W. Grise, Dept. of IET, Morehead State University, Morehead, KY; Application of the Operational Transconductance Amplifier (OTA) to Voltage-controlled Amplifiers and Active Filters; 10 pages "NO MONTH" "NO YEAR"
2.	<i>D</i>	Axel Thomsen, Dan Kasha, Wai Lee, Crystal Semiconductor Division, Cirrus Logic, Inc., Austin, Texas; A Five Stage Chopper Stabilized Instrumentation Amplifier Using Feedforward Compensation; 1998; 4 pages "NO MONTH"
3.	<i>D</i>	Axel Thomsen, Edwin de Angel, Sherry Ziachong Wu, Aryesh Amar, Lei Wang, Wai Lee, WA 20.1 A DC Measurement IC with 130nV _{pp} Noise in 10Hz; 2000 IEEE International Solid-State Circuits Conference; 9 pages "NO MONTH" "NO YEAR"
4.	<i>D</i>	Axel Thomsen, Edwin de Angel, Sherry Ziachong Wu, Aryesh Amar, Lei Wang, Wai Lee, WA 20.1 A DC Measurement IC with 130nV _{pp} Noise in 10Hz; ISSCC 2000/Session 20/Oversampled Converters/ Paper WA 20.1 ISSCC 2000 Slide Supplement; 5 pages "NO MONTH" "NO YEAR"

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Date Considered:

11/10/05

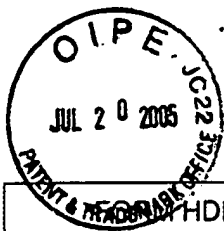
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5.	<i>P</i>	M. Moyal, M. Groepl, H. Werker, G. Mitteregger, J. Schambacher; Xignal Technologies AG, Munich, Germany; 23.6 A 700/900mW/Channel CMOS Dual Analog Front-End IC for VDSL with Integrated 11.5/14.5dBm Line Drivers; 2003 IEEE International Solid-State Circuits Conference; ISSCC 2003/Session 23/Mixed-Signal and Wireline Techniques; 10 pages "NO MONTH"
6.	<i>P</i>	M. Moyal, M. Groepl, H. Werker, G. Mitteregger, J. Schambacher; Xignal Technologies AG, Munich, Germany; A 700/900mW/Channel CMOS Dual Analog Front-End IC for VDSL with Integrated 11.5/14.5dBm Line Drivers; Outline, Overview 24 pages "NO MONTH" "NO YEAR"

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Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	<i>E</i>	10/788,618	2/24/04	Farbod Aram	327/16	02/27/2004

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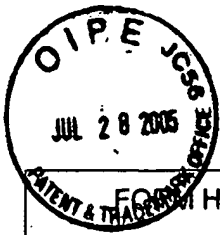
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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	
1.	<i>D</i>	Bhupendra K. Ahuja, "An Improved Frequency Compensation Technique for CMOS Operational Amplifiers", IEEE Journal of Solid-State Circuits, Vol. SC-18, No. 6, December 1983, pp. 629-633.

Examiner: <i>Cam Mac</i>	Date Considered: 11/10/05
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1.	<i>SP</i>	10/788,618		Aram, Farbod		02/27/2004
2.	<i>SP</i>	11/097,530		Aram, Farbod		04/01/2005
3.	<i>SP</i>	11/170,926		Aram, Farbod		06/30/2005
4.	<i>SP</i>	6,690,229	02/2004	Rudolph, Jochen		

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